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IN RE APPLICATION OF:

Hiroshi NAKAMURA, et al.

GROUP: 2824

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EXAMINER: NGUYEN, NAM THANH

FOR:

SEMICONDUCTOR MEMORY

SUBMISSION OF SUPPLEMENTAL APPLICATION DATA SHEET

Office of Initial Patent Examination Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the aboveidentified application for the purpose of correcting the first inventor's family name.

Respectfully Submitted,

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